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				Application Number	10/771678
				Filing Date	2/4/2004
				First Named Inventor	Gerard M. Col
				Group Art Unit	2181
				Examiner Name	Jesse R. Moll
				Attorney Docket Number	CNTR.2094
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
JM		5812813	09-22-1998	Henry et al.	
JM		6393555	05-21-2002	Meier et al.	
JM		6065103	05-16-2000	Tran et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
JM		EP0402787 A2	12-19-1990	Charles P. Ryan		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JM		Jim Handy, The Cache Memory Book, 2nd Edition, 1998, pages 47 and 222.	
JM		FARCY A ET AL: "Dataflow analysis of branch mispredictions and its application to early resolution of branch outcomes" MICRO-31. PROCEEDINGS OF THE 31ST. ANNUAL ACM/IEEE INTERNATIONAL SYMPOSIUM ON MICROARCHITECTURE. DALLAS, TX, NOV. 30 - DEC. 2, 1998, PROCEEDINGS OF THE ANNUAL ACM/IEEE INTERNATIONAL SYMPOSIUM ON MICROARCHITECTURE, LOS ALMITOS, CA : IEEE COMP. SOC, US, 30 November 1998 (1998-11-30), pages 59-68, XP010318259 ISBN: 0-8186-8609-X * page 64, paragraph 3 - page 66, paragraph 3.3*	
JM		IA-32 Intel Architecture Software Developer's Manual. Volume 2: Instruction Set Reference. 2001, pages 3-354 to 3-357	

Examiner Signature	/Jesse Moll/	Date Considered	06/21/2006
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